

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	
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Pierre BLANCHARD	:	Confirmation No.7171
	:	
U.S. Patent Application No. 10/582,711	:	Group Art Unit: 2892
	:	
Filed: June 12, 2006	:	Examiner: Jones, Eric W.
For: METHOD FOR PRODUCING ELECTRONIC CHIPS CONSISTING OF THINNED SILICON		

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

By Official Action mailed April 24, 2008 restriction to one of the following inventions is required under 35 USC 121:

- I. **Group I, claim(s) 16-25, drawn to a method for the fabrication of electronic chips from a semiconductor wafer comprising, on its front face, a thin active layer of semiconductor material.**
- II. **Group II, claim(s) 26-29, drawn to a color image sensor.**

In response, Applicants hereby elect **Group I, claims 16-25** for examination in this case. This election is made with traverse - see below.